

*You Choose, We Do it*

**St. JOSEPH'S COLLEGE OF ENGINEERING**



*We Make You Shine*

**St. JOSEPH'S INSTITUTE OF TECHNOLOGY**

St. Joseph's Group of Institutions

OMR, CHENNAI - 600 119



**SUMMARY OF CLASS WORK**  
RECORD OF ATTENDANCE AND ASSESSMENT

Name of the Staff: S. P. Vedavalli

Department of the Staff: EEE

Semester From: AUG 2020 To OCT 2020

Class & Branch: III EEE - C

Code No. / Subject Name: EEB559 - ce Electronics





**OBJECTIVES:**

To impart knowledge on the following Topics

- Different types of power semiconductor devices and their switching
- Operation, characteristics and performance parameters of controlled rectifiers Operation, switching techniques and basics topologies of DC-DC switching regulators.
- Different modulation techniques of pulse width modulated inverters and to understand harmonic reduction methods.
- Operation of AC

**UNIT I POWER SEMI-CONDUCTOR DEVICES**

9

Study of switching devices, SCR, TRIAC, GTO, BJT, MOSFET, IGBT and IGCT- Static characteristics: SCR, MOSFET and IGBT - Triggering and commutation circuit for SCR, Introduction to Driver and snubber circuits.

**UNIT II PHASE-CONTROLLED CONVERTERS**

9

2-pulse, 3-pulse and 6-pulse converters- performance parameters -Effect of source inductance- Firing Schemes for converter-Dual converters, Applications-light dimmer, Excitation system, Solar PV systems.

**UNIT III DC TO DC CONVERTERS**

9

Step-down and step-up chopper-control strategy- Introduction to types of choppers-A, B, C, D and E -Switched mode regulators- Buck, Boost, Buck- Boost regulator, Introduction to Resonant Converters, Applications-Battery operated vehicles.

**UNIT IV INVERTERS**

9

Single phase and three phase voltage source inverters (both 1200 mode and 1800 mode)- Voltage & harmonic control--PWM techniques: Multiple PWM, Sinusoidal PWM, modified sinusoidal PWM - Introduction to space vector modulation -Current source inverter, Applications-Induction heating, UPS.

**UNIT V AC TO AC CONVERTERS**

9

Single phase and Three phase AC voltage controllers-Control strategy- Power Factor Control - Multistage sequence control -single phase and three phase cyclo converters - Introduction to Matrix converters, Applications -welding .

**TOTAL : 45 PERIODS****OUTCOMES:**

- Ability to analyse AC-AC and DC-DC and DC-AC converters.
- Ability to choose the converters for real time applications.

**TEXT BOOKS:**

1. M.H. Rashid, 'Power Electronics: Circuits, Devices and Applications', Pearson Education, Third Edition, New Delhi, 2004.
2. P.S.Bimbra "Power Electronics" Khanna Publishers, third Edition, 2003.
3. Ashfaq Ahmed 'Power Electronics for Technology', Pearson Education, Indian reprint, 2003.

**REFERENCES**

1. Joseph Vithayathil, 'Power Electronics, Principles and Applications', McGraw Hill Series, 6th Reprint, 2013.
2. Philip T. Krein, "Elements of Power Electronics" Oxford University Press, 2004 Edition.
3. L. Umanand, "Power Electronics Essentials and Applications", Wiley, 2010.
4. Ned Mohan Tore. M. Undel and, William. P. Robbins, 'Power Electronics: Converters, Applications and Design', John Wiley and sons, third edition, 2003.
5. S.Rama Reddy, 'Fundamentals of Power Electronics', Narosa Publications, 2014.
6. M.D. Singh and K.B. Khanchandani, "Power Electronics," Mc Graw Hill India, 2013.
7. JP Agarwal, "Power Electronic Systems: Theory and Design" 1e, Pearson Education, 2002.





# LESSON PLAN

Unit: 7

From 12/8/2020 To 27/8/2020

No. of Hours: 09

Target Period: 9

## Unit-I: POWER SEMI-CONDUCTOR DEVICES

Planned Periods: 12

S.NO	Topics to be Covered	Course Outcome	Hours Required	Text/Reference Book	Teaching Aid	Knowledge Level
1.	Introduction to power electronics switching devices	C303.1	1	M.H.Rashid, P.S.Bimbra "Power Electronics"	PPT	R,U
2.	Basic Operation, V-I characteristics and switching characteristics of SCR.		1		Black Board	R,U,An
3.	Basic Operation, V-I characteristics and switching characteristics of TRIAC.		1		Black Board	R,U,An
4.	Basic Operation, V-I characteristics and switching characteristics of GTO and BJT.		1		Black Board	R,U,An
5.	Basic Operation, V-I characteristics and switching characteristics of MOSFET.		1		Black Board	R,U,An
6.	Basic Operation, V-I characteristics and switching characteristics of IGBT.		2		PPT	R,U,An
7.	Basic Operation, V-I characteristics and switching characteristics of IGCT.		1		Black Board	R,U,An
8.	Various types of commutation circuits and triggering circuits for SCR		2		Black Board	R,U,An
9.	Introduction of Driver and snubber circuit.		1		Black Board	R,U,An

Assignment submission: 30/8/2020

30/8/2020

Seminar/Tutorial: 28/8/2020

Production department

Internal Assessment 1: 2/9/2020

2/9/2020

# LESSON PLAN

Unit: 11

From 01/09/2020 to 16/09/2020

No. of Hours: 09

## Unit-II PHASE CONTROLLED CONVERTERS

Target Period: 9

Planned Periods: 12

S.NO	Topics to be Covered	Course Outcome	Hours Required	Text/Reference Book	Teaching Aid	Knowledge Level
1.	Analysis and basic operation of single phase fully controlled converter.	C303.2	1	M.H.Rashid, "Power Electronics"  P.S.Bimbhra "Power Electronics"	Black Board	R.U.
2.	Analysis and basic operation of single phase Half controlled converter.		2		Black Board	R.U,An.
3.	Analysis and basic operation of Three phase Half wave controlled converter.		1		Black Board	R.U,An.
4.	Analysis and basic operation of Three phase Full wave controlled converter		1		PPT	R.U,An.
5.	Effect of source inductance in single phase converter		2		Black Board	R.U,An.
6.	Effect of source inductance in Three phase converter		1		Black Board	R.U,An.
7.	Various Types of gate circuit schemes for phase control converters.		1		PPT	R.U,An.
8.	Analysis and basic Working of Dual converter.		1		Black Board	R.U,An.
9.	Applications-light dimmer, Excitation system, Solar PV systems		1		Black Board	R.U,An.
10.	Tutorial		1		Black Board	R.U,An.

Assignment submission: 24/9/2020

Seminar/Tutorial: -

Internal Assessment 2: 25/9/2020



# LESSON PLAN

Unit : IV

From 05/10/2020 to 14/10/2020

No. of Hours : 09

## Unit-IV: INVERTER

Target Period: 9

Planned Periods: 12

.NO	Topics to be Covered	Course Outcome	Hours Required	Text/Reference Book	Teaching Aid	Knowledge Level
1.	Design and analysis of single phase square wave voltage source inverter		1		PPT	R,U,An,
2.	Design and analysis of Three phase voltage source inverter in 120 degree conduction		2		PPT	R,U,An,A
3.	Design and analysis of Three phase voltage source inverter in 180 degree conduction		1		PPT	R,U,An,A
4.	Voltage control and Harmonics elimination by Single pulse and Multi pulse PWM technique		2	P.S.Bimbra "Power Electronics"	Black Board	R,U,An,
5.	Voltage control and Harmonics elimination by Sinusoidal PWM technique	C303.4	1		Black Board	R,U,An,
6.	Voltage control and Harmonics elimination by Space vector modulation method		1	M.H.Rashid, "Power Electronics"	Black Board	R,U,An,
7.	Introduction to current source inverter and analysis of Auto sequential commutate current source inverter		1		PPT	R,U,An,
8.	Comparison of current source inverter with voltage source inverter.		1		Black Board	R,U
9.	Applications-Induction heating, UPS.		1		Black Board	U,An
10.	Tutorial		1		Black Board	R,U,An,

Assignment submission: —

Seminar/Tutorial: —

Production from Heating

Internal Assessment 4: —

UNIT: 5

From 15/10/2020 TO 30/10/2020

No. of hours: 09

**Unit-V: AC TO AC CONVERTERS**

Target Period: 9

Planned Periods:12

S.N	Topics to be Covered	Course Outcome	Hours Required	Text/Reference Book	Teaching Aid	Knowledge Level
1.	Introduction to single phase Ac to Ac voltage converter.		1	P.S.Bimbra "Power Electronics"  M.H.Rashid, "Power Electronics"	Power Point Presentation	R,U,An,
2.	ON-OFF and phase control methodology for AC to AC voltage converter		2		Power Point Presentation	R,U,An,
3.	Single stage sequence control methodology for AC to AC voltage converter		1		Black Board	R,U,A,An
4.	Multi stage sequence control methodology for AC to AC voltage converter		1		Black Board	R,U,An,
5.	Introduction to single phase cyclo converter	C303.5	2		Black Board	R,U,An,
6.	Introduction to Three phase cyclo converter		1		Black Board	R,U,An,
7.	Analysis of three phase to single phase cyclo converter		1		Power Point Presentation	R,U,An,
8.	Analysis and working of Matrix converter		1		Black Board	R,U,An,
9.	Applications -welding		1		Black Board	U,An
10.	Tutorial		1		Black Board	R,U,An,

Assignment submission: ~

Seminar/Tutorial: ~

Model Exam: 4/11/2020

**Web source Reference**

S.No	UNIT	TOPIC	Ref / Link
1	2	Single phase fully controlled rectifier	NPTEL
2	3	Operation buck converter	NPTEL

**Self Study topics**

S.No	UNIT	TOPIC	Text / Ref book
1	1	V-I characteristics of Power diode.	T1/R4
2	2	Compare the half controlled rectifier with fully controlled rectifier	T1/R4

*S. S. Reddy*  
STAFF SIGNATURE

*S. S. Reddy*  
HOD SIGNATURE

PRINCIPAL



# LESSON PLAN

Unit : III

From 19/2020 To 21/10/2020

No. of Hours : 09

## Unit-III: DC TO DC CONVERTER

Target Period: 9

Planned Periods: 12

S.N O	Topics to be Covered	Course Outcome	Hours Required	Text/Reference Book	Teaching Aid	Knowledge Level
1.	Design and analysis of step down chopper	C303.3	1	M.H.Rashid, "Power Electronics"	PPT	R,U,An,
2.	Design and analysis of step Up chopper		1		Black Board	R,U,An,
3.	Introduction to types of choppers-A, B, C, D and E		1		PPT	R,U,An,
4.	Analysis of Buck converter.		2		PPT	R,U,An,A
5.	Analysis of Boost converter.		1		Black Board	R,U,An,A
6.	Analysis of Buck -Boost converter.		1		Black Board	R,U,An,A
7.	Time ratio control and current limiting control for chopper		1		PPT	R,U,An,
8.	Analysis Of Forced Commutated chopper and voltage commutated chopper		1		Black Board	R,U,An,
9.	Design and analysis of Resonant Converter		1		Black Board	R,U,An,
10.	Applications-Battery operated vehicles.		1		Black Board	U,An,
11.	Tutorial		1		Black Board	R,U,An,

Assignment submission: 7/10/2020

Seminar/Tutorial: 2/10/2020

Internal Assessment 3: 21/10/2020

Application - Battery operated vehicles



## DAILY RECORD OF CLASS WORK

Month & Year : *Aug & Sept 2020*

Date*	Day	Allotted Period	Period Handled	Reason for Alteration
	Monday			
	Tuesday			
<i>12/8/2020</i>	Wednesday	<i>31</i>	<i>-</i>	<i>Orientation Program</i>
<i>13/8/2020</i>	Thursday	<i>33</i>	<i>33</i>	
<i>14/8/2020</i>	Friday	<i>-</i>	<i>-</i>	
<i>15/8/2020</i>	Saturday	<i>-</i>	<i>-</i>	
<i>17/8/2020</i>	Monday	<i>31</i>	<i>31</i>	
<i>18/8/2020</i>	Tuesday	<i>33</i>	<i>33</i>	
<i>19/8/2020</i>	Wednesday	<i>31</i>	<i>31</i>	
<i>20/8/2020</i>	Thursday	<i>33</i>	<i>33</i>	
<i>21/8/2020</i>	Friday	<i>-</i>	<i>-</i>	
<i>22/8/2020</i>	Saturday	<i>-</i>	<i>-</i>	
<i>24/8/2020</i>	Monday	<i>32</i>	<i>32</i>	
<i>25/8/2020</i>	Tuesday	<i>34</i>	<i>34</i>	
<i>26/8/2020</i>	Wednesday	<i>31</i>	<i>31</i>	
<i>27/8/2020</i>	Thursday	<i>34</i>	<i>34</i>	
<i>28/8/2020</i>	Friday	<i>-</i>	<i>-</i>	
<i>29/8/2020</i>	Saturday	<i>-</i>	<i>-</i>	

31/8/2020	Monday	S1	-	Unit Test - 1
01/09/2020	Tuesday	S3	S3	
02/09/2020	Wednesday	S1	-	UNIT Test - 1
03/09/2020	Thursday	S3	S3	
04/09/2020	Friday	-	-	
05/09/2020	Saturday	-	-	
07/09/2020	Monday	S1	S1	
08/09/2020	Tuesday	S3	S3	
09/09/2020	Wednesday	S1	S1	
10/09/2020	Thursday	S3	S3	
11/09/2020	Friday	-	-	
12/09/2020	Saturday	-	-	
14/09/2020	Monday	S1	S1	
15/09/2020	Tuesday	S3	S3	
16/09/2020	Wednesday	S1	S1	
17/09/2020	Thursday	S3	S3	
18/09/2020	Friday	-	-	
19/09/2020	Saturday	-	3 to 4	TO Complete syllabus

\* Holidays / CL and OD days to be mentioned against the corresponding dates

Signature of HOD :







































## SUMMARY OF CLASS WORK

DATE: 12/8/2020

PERIOD: 33

TOTAL NO. OF CLASS 01

UNIT: 7

Introduction to Power Electronics system - Advantages & Disadvantages of PES - Applications.

Power Semiconductor devices - Introduction & its types.

DATE: 17/8/2020

PERIOD: 31

TOTAL NO. OF CLASS 02

UNIT: 7

SCR - Symbol, Modes of Operation,  $V_T$  & switching characteristics  
Turn-on Methods & Two-transistor analogy, Advantages & Disadvantages & Applications.

DATE: 18/8/2020

PERIOD: 33

TOTAL NO. OF CLASS 03

UNIT: 7

TRINC - Symbol,  $V_T$  characteristics and Modes of Operation - Applications.



## SUMMARY OF CLASS WORK

DATE: 19/8/2020

PERIOD: 81

TOTAL NO. OF CLASS 04

UNIT: 1

BJT - Symbol, Operation,  $V_{CE}$  characteristics - switching charac.  
transistor as a switch - Operating region, Advantages &  
disadvantages - Application.

DATE: 20/8/2020

PERIOD: 83

TOTAL NO. OF CLASS 05

UNIT: 1

MOSFET - Symbol, Operation, Types - Enhancement Types &  
Depletion modes -  $V_{DS}$  characteristics: Transfer & Drain characteristics  
switching characteristics: Turn-on & Turn-off characteristics.

DATE: 24/8/2020

PERIOD: 82

TOTAL NO. OF CLASS 06

UNIT: 1

JGIBT - Symbol, Operation,  $V_{CE}$  characteristics:- Transfer &  
collector Characteristics - switching Characteristics - Turn-on & Turn-off  
Characteristics.  
Semiconductors & Constructional details of JGIBT.

Sign of HOD with Date



## SUMMARY OF CLASS WORK

DATE: 25/8/2020 PERIOD: 84 TOTAL NO. OF CLASS 07 UNIT: 7

Gr10 - symbol, operations, V<sub>i</sub> characteristics & switching charac.

IGCT - symbol, operations, V<sub>i</sub> characteristics & switching characteristics & Applications.

DATE: 26/8/2020 PERIOD: 81 TOTAL NO. OF CLASS 08 UNIT: 7

Triggering circuits - R, RC & UJT Triggering circuits.  
Commutation circuits - Introduction & its Types.

Class A, class B & class C Commutation - circuit diagram & its wave forms.

DATE: 27/8/2020 PERIOD: 84 TOTAL NO. OF CLASS 09 UNIT: 7

class D Commutation circuits - Introduction & its wave forms  
class E Commutation - Natural commutation.

Introduction to Drive circuits & Snubber circuits.



## SUMMARY OF CLASS WORK

DATE: 01/09/2020 PERIOD: 93 TOTAL NO. OF CLASS 01 UNIT: II

Introduction to phase - Controlled Rectifiers - Applications  
Single phase half-wave Rectifier (one pulse converter) - Operations  
with R, RL, RL with FWD and RLE load. Expression for average o/p voltage.

DATE: 03/09/2020 PERIOD: 93 TOTAL NO. OF CLASS 02 UNIT: II

Two pulse Converter - 1 $\phi$  full wave converter (mid point & Bridge-type  
Configuration) - Operations with R, RL and RLE loads - Expression for o/p  
Voltage - performance parameters of 1 $\phi$  full wave converter.

DATE: 07/09/2020 PERIOD: 91 TOTAL NO. OF CLASS 03 UNIT: II

Single phase semiconductor - Symmetrical & Asymmetrical Configuration  
Circuit diagram, waveforms & Expression for average & rms value  
of o/p voltage.

Sign of HOD with Date



## SUMMARY OF CLASS WORK

DATE: 08/09/2020

PERIOD: 53

TOTAL NO. OF CLASS 04

UNIT: II

Three Phase Half-wave Converter (3 Pulse Converter) -  
Operation with  $R_L$  &  $R_L E$  load - Circuits diagram & its waveforms.  
Expression for output voltage.

DATE: 09/09/2020

PERIOD: 51

TOTAL NO. OF CLASS 05

UNIT: II

Three phase Semi-converter - Operation with  $R_L$  load,  
 $R_L$  with  $FWD$  and  $R_L E$  load - Expression for output voltage.

DATE: 10/09/2020

PERIOD: 53

TOTAL NO. OF CLASS 06

UNIT: II

Three phase full converter (Six pulse Converter) -  
Operation with  $R_L$ ,  $R_L$  load &  $R_L E$  load - Expression for o/p  
Voltage - Performance parameters of 3 $\phi$  converter.



## SUMMARY OF CLASS WORK

DATE: 14/09/2020

PERIOD: 81

TOTAL NO. OF CLASS 07

UNIT :

11

Effect of source impedance on 1 $\phi$  full converter & 3 $\phi$  full converter - Expression of average o/p voltage.

DATE: 15/09/2020

PERIOD: 83

TOTAL NO. OF CLASS 08

UNIT :

11

Dual Converter - Types - Non-circulating & circulating Types  
dual Converter - Operations & waveforms.

DATE: 16/09/2020

PERIOD: 81

TOTAL NO. OF CLASS 09

UNIT :

11

Applications - Light Dimmers, circuit diagram & operation.  
Excitation system - Ac, Dc & static Excitation systems.  
Solar PV system - stand alone & Grid-Connected system.

Sign of HOD with Date





### SUMMARY OF CLASS WORK

DATE: 28/10/20 PERIOD: 31 TOTAL NO. OF CLASS 7 UNIT: 7

Matrix Converter - circuit diagram - Operations - Advantages & disadvantages

DATE: 29/10/20 PERIOD: 32 TOTAL NO. OF CLASS 8 UNIT: 7

Applications - welding - Types & its uses.

DATE: 30/10/20 PERIOD: 32 TOTAL NO. OF CLASS 9 UNIT: 7

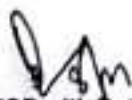
Tutorial - problems on single phase & three phase ac voltage controller.

### SUMMARY OF CLASS WORK

DATE: PERIOD: TOTAL NO. OF CLASS UNIT:

DATE: PERIOD: TOTAL NO. OF CLASS UNIT:





DATE: PERIOD: TOTAL NO. OF CLASS UNIT:

  
Sign of HOD with Date



# RESULT ANALYSIS

## TERM - I

S.No	Description	IAE - I	IAE - II
1	Date of Exam	02/09/2020	25/9/2020
2	Total No. of Students	52	52
3	No of Students Attended	50	52
4	No of Students Passed	50	51
5	Percentage of Pass	100%	98.07%
Signature of the Staff		: 	: 
Name of the Staff		: S.P. Vedavalle	: S.P. Vedavalle
Signature of the HOD of the Concerned Department with Date (After distributing the answer scripts)		: 	: 

No. of Students	Range of Marks				
	IAE - I	IAE - II	00	1	2
02	-	4	11	11	14
00	0	1	2	29	16



No. of Students (IAE I & II Combined)	Range of Marks				
	0-44	45-60	61-70	71-80	81-90
2	0	6	19	20	5

Signature of the HOD of the Concerned Department



# RESULT ANALYSIS

## TERM - II

S.No	Description	IAE - III	IAE - IV
1	Date of Exam	21/10/2020	
2	Total No. of Students	52	
3	No of Students Attended	52	
4	No of Students Passed	52	
5	Percentage of Pass	100%	
Signature of the Staff		: 	
Name of the Staff		: S.P. Vedavalle	
Signature of the HOD of the Concerned Department with Date (After distributing the answer scripts)		: 	

No of Students	Range of Marks				
	IAE - III	IAE - IV	0	8	14
0	0	8	23	14	5
-	-	-	-	-	-

No. of Students (IAE III & IV Combined)	Range of Marks				
	0-44	45-60	61-70	71-80	81-90
-	-	-	-	-	-

Signature of the HOD of the Concerned Department





# RESULT ANALYSIS

## MODEL EXAMINATION

1	Date of Exam	:	4/11/2020
2	Total No. of Students	:	52
3	No of Students Attended	:	52
4	No of Students Passed	:	52
5	Percentage of Pass	:	100 %

## RESULT ANALYSIS

Range of Marks	0-44	45-60	61-70	71-80	81-90	91-100
No. of Students	0	0	1	0	15	36

Signature of the Staff

: *Richard S. S.*

Name of the Staff

: S. P. Vedavalli.

Signature of the HOD of the Concerned

Department with Date


: *[Signature]*

(After distributing the answer scripts)

## SYLLABUS COVERAGE

Unit No.	Started on	Completed on	No. of hours	Sign of staff with Date	Sign of HOD with Date
I	12/8/2020	27/8/2020	09	<i>Richard S. S.</i> 21/8/2020	<i>[Signature]</i>
II	01/09/2020	16/09/2020	09	<i>Richard S. S.</i> 16/9/2020	<i>[Signature]</i>
III	17/09/2020	02/10/2020	09	<i>Richard S. S.</i> 2/10/2020	<i>[Signature]</i>
IV	05/10/2020	14/10/2020	09	<i>Richard S. S.</i> 14/10/2020	<i>[Signature]</i>
V	15/10/2020	30/10/2020	09	<i>Richard S. S.</i> 20/10/2020	<i>[Signature]</i>
Contents Beyond syllabus:					
1)	UNIT - I	SCR protection - 20/8/2020 -			<i>[Signature]</i>
2)	UNIT - IV	Multilevel Inverter - 12/10/2020			<i>[Signature]</i>
3)	UNIT - II	conducted Workshop on Solar power Technologies - 11/10/2020			<i>[Signature]</i>

## REMARKS

S.No.	Date	Remarks/Comments/Deviations, if any	HOD Signature with Date
01.	20/10/2020	Conducted special classes on 02/10/2020, 10/10/2020, 16/10/2020 & 31/10/2020 to complete syllabus in unit 3, 4 & 5.	





**St. JOSEPH'S COLLEGE OF ENGINEERING**  
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OMR, Chennai - 119  
Department of Electrical and Electronics Engineering



**EE8552 POWER ELECTRONICS**  
**MODEL EXAM**  
**ANSWER ALL QUESTIONS**  
**PART-A**

**(50\*2=100 Marks)**

1. List the different methods to turn on SCR.
2. Define threshold voltage of power MOSFET.
3. Mention the advantages of GTO over SCR.
4. Mention the merits and demerits of GTO.
5. How is  $di/dt$  and  $dv/dt$  protection provided in SCR?
6. Mention the advantages of 'RC' triggering over 'R' triggering.
7. What is meant by commutation of a SCR and list its types.
8. Compare MOSFET and BJT.
9. What is the use of snubber circuit?
10. Define pinch off voltage of MOSFET.
11. Write the relation between firing angle and extinction angle in single phase fully controlled rectifier when operating with RL load.
12. Classify the different types of controlled rectifier.
13. What is the function of freewheeling diode and state its advantages?
14. What is meant by phase control?
15. Mention the disadvantages of dual converter with circulating current mode of operation?
16. Why is power factor of semi converter better than full converter?
17. Mention the effect of source inductance in converters.
18. Define the input power factor.
19. What is the difference between symmetric and asymmetric semiconductor configuration?
20. What is firing angle and overlap angle?
21. What is meant by time ratio/ duty cycle or PWM control (duty cycle) of a DC chopper?
22. What is the effect of load inductance on the load current waveforms in the case of DC chopper?
23. What is constant frequency control of chopper?
24. What are the disadvantages of frequency modulated chopper?
25. Distinguish between time ratio control and current limit control employed in a DC chopper.



**St. JOSEPH'S COLLEGE OF ENGINEERING**

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OMR, Chennai - 119

Department of Electrical and Electronics Engineering

**EEASSI POWER ELECTRONICS**

Internal Assessment Exam III

ANSWER ALL QUESTIONS

Multiple choice questions(10\*1 =10Marks)

- In a VSI (Voltage source inverter)
  - the internal impedance of the DC source is negligible
  - the internal impedance of the DC source is very very high
  - the internal impedance of the AC source is negligible
  - the IGBTs are fired at 0 degrees.
- VSI's using GTOs are turned off by
  - load commutation
  - line commutation
  - applying a negative gate pulse
  - removing the base signal
- The voltage in a single phase half wave inverter varies between
  - $V_s$  and 0
  - $V_s/2$  and 0
  - $V_s/2$  and  $-V_s/2$
  - $V_s$  and  $-V_s$
- A single phase half bridge inverter has load  $R = 2 \Omega$  and a dc voltage source  $V_s = 115$  V. Find the power delivered to the load due to the fundamental component.
  - 516 kW
  - 51.61 kW
  - 5.361 kW
  - 516 W
- \_\_\_\_\_ is the measure of the contribution of any individual harmonic to the inverter output voltage.
  - THD
  - Distortion Factor
  - Harmonic Factor
  - TUF
- A single phase inverter gives rms value of output voltage as 115 V and the fundamental output voltage of as 101.5 V. Find the THD (Total Harmonic Distortion).
  - 0.4 %
  - 40.8 %
  - 48.3 %
  - 4.83 %
- In sinusoidal pulse width modulation, the comparator output is high when the
  - triangular wave has magnitude higher than the sinusoidal wave
  - sinusoidal wave has magnitude higher than the triangular wave
  - triangular wave has magnitude equal to the sinusoidal wave
  - none of the mentioned
- Which type of chopper is used in the regenerative braking of DC motors?
  - type A
  - type B
  - type C
  - type D
- For a type D chopper, the average value of output voltage will be positive when
  - $T_{on} = T_{off}$
  - $T_{on} < T_{off}$
  - $T_{off} = 0$
  - $T_{on} > T_{off}$
- What is the expression for load voltage when the chopper is operated in the second quadrant?
  - $V_s$
  - E
  - 0
  - $E + L \frac{di}{dt}$





**St. JOSEPH'S COLLEGE OF ENGINEERING**  
St. Joseph's Group of Institutions

OMR, Chennai - 119

Department of Electrical and Electronics Engineering  
EE8852 POWER ELECTRONICS

CLASS TEST I



**ANSWER ALL QUESTIONS**

Multiple choice questions(10\*1 =10Marks)

1. A power transistor is a \_\_\_\_\_ device.
    - a) two terminal, bipolar, voltage controlled
    - b) two terminal, unipolar, current controlled
    - c) three terminal, unipolar, voltage controlled
    - d) three terminal, bipolar, current controlled
  2. A power BJT is used as a power control switch by biasing it in the cut off region (off state) or in the saturation region (on state). In the on state
    - a) both the base-emitter & base-collector junctions are forward biased
    - b) the base-emitter junction is reverse biased, and the base collector junction is forward biased
    - c) the base-emitter junction is forward biased, and the base collector junction is reverse biased
    - d) both the base-collector & the base-emitter junctions are reverse biased
  3. At turn-on the initial delay or turn on delay is the time required for the
    - a) input inductance to charge to the threshold value
    - b) input capacitance to charge to the threshold value
    - c) input inductance to discharge to the threshold value
    - d) input capacitance to discharge to the threshold value
  4. Choose the correct statement(s)
    - i) The gate circuit impedance of MOSFET is higher than that of a BJT
    - ii) The gate circuit impedance of MOSFET is lower than that of a BJT
    - iii) The MOSFET has higher switching losses than that of a BJT
    - iv) The MOSFET has lower switching losses than that of a BJT
    - a) Both i & ii
    - b) Both ii & iv
    - c) Both i & iv
    - d) Only ii
  5. The structure of the IGBT is a
    - a) P-N-P structure connected by a MOS gate
    - b) N-N-P-P structure connected by a MOS gate
    - c) P-N-P-N structure connected by a MOS gate
    - d) N-P-N-P structure connected by a MOS gate
6. An IGBT is also known as
    - a) MOCGT (Metal oxide insulated gate transistor)
    - b) COMFET (Conductively modulated FET)
    - c) GEMFET (Gate modulated FET)
    - d) all of the mentioned
  7. A thyristor can be brought from the forward conduction mode to forward blocking mode by
    - a) the dv/dt triggering method
    - b) applying a negative gate signal
    - c) applying a positive gate signal
    - d) applying a reverse voltage across anode-cathode terminals
  8. During the transition time or turn-on time
    - a) The forward anode voltage decreases from 90 % to 10 % & the anode current also decreases from 90 to 10 % of the initial value
    - b) The forward anode voltage increases from 10 % to 90 % & the anode current also increases from 10 % to 90 % of the initial value
    - c) The forward anode voltage decreases from 90 % to 10 % & the anode current increases from 10 % to 90 % of the initial value
    - d) The forward anode voltage increases from 10 % to 90 % & the anode current decreases from 90% to 10% of the initial value
  9. Consider the two transistor analogy of SCR, if  $\alpha_1$  &  $\alpha_2$  are the common base current gains of both the transistors then to turn-on the device
    - a)  $\alpha_1 + \alpha_2$  should approach zero
    - b)  $\alpha_1 \times \alpha_2$  should approach unity
    - c)  $\alpha_1 - \alpha_2$  should approach zero
    - d)  $\alpha_1 + \alpha_2$  should approach unity
  10. Choose the correct statement:  
GTOs have \_\_\_\_\_ as compared to the CTs.
    - a) less on-state voltage drop
    - b) less gate drive losses
    - c) higher reverse blocking capabilities
    - d) faster switching speed

**PART-A**

(5\*2=10 Marks)

11. Define threshold voltage of power MOSFET.
12. Mention the advantages of GTO over SCR.
13. Mention the advantages of RCT triggering over R triggering.
14. List the different methods to turn on SCR.
15. Define forward current gain and current gain of transistor.